Sanjukta Bhanja received her Bachelor's degree in Electrical Engineering from Jadavpur University, Calcutta in 1991 and her Masters Degree from Indian Institute of Science, Bangalore in 1994. She finished her PhD degree in Computer Science and Engineering in 2002 from the University of South Florida, Tampa. She is currently an associate professor in the Department of Electrical Engineering at the University of South Florida. Her primary research focus is in non-CMOS nano-computing, exploring novel state variables, alternate computing paradigm with heterogeneous devices, VLSI design automation with emphasis on data-driven uncertainties, trade-off of error, power, and reliability at various levels of design abstractions.

She has published more than ninety publications in top-tier peer-reviewed journals and conferences in the VLSI and nano-electronics areas. She has been an Associate Editor of the IEEE Transactions on VLSI Systems (2011-2015) and ACM Journal on Emerging Technologies in Computing Systems (current). She has served on the Technical Program Committees of various IEEE and ACM conferences; namely she was a TPC member, trak co-chair and track chair of "Emerging Technology track" in IEEE DATE'12-14. She has served as the Technical Program Co-Chair of ACM GLSVLSI 2008, IEEE ISVLSI 2009; and as the General Co-Chair of ACM GLSVLSI 2009 and IEEE ISVLSI 2014. She organized (National Science Foundation sponsored) a conference on "Field-coupled Nano-computing" in 2013 that connected and evaluated research progress in the area of Field-coupled computing. In 2014, she organized CRAW/CDC/NSF sponsored discipline-specific workshop addressing diversity issues in Design Automation for Emerging Technology. She joined [illegible] Detailed Updated CV

Research Interests

Selected Publications